

## Kadi Sarva Vishwavidyalaya

# Faculty of Engineering & Technology Second Year Bachelor of EC Engineering

Subject Code: EC308- N Subject Title: SEMINAR

### **Course Objective:**

- To make the students familiar about the latest developments in the areas of electronics and communication engineering.
- To make them learn the modern modes of communication and presentation through computerized systems and reports.
- To prepare students for taking up new and completely unfamiliar challenges.
- To make them learn to how to use the e resources fully available on-line.

### **Teaching Scheme (Credits and Hours)**

| Teaching scheme |     |     |       | Total  | l Evaluation Scheme |       |       |       |        |       |
|-----------------|-----|-----|-------|--------|---------------------|-------|-------|-------|--------|-------|
| L               | T   | P   | Total | Credit | Theory              |       | IE    | CIA   | Pract. | Total |
| Hrs             | Hrs | Hrs | Hrs   |        | Hrs                 | Marks | Marks | Marks | Marks  | Marks |
| 00              | 00  | 02  | 02    | 01     | 00                  | 00    | 00    | 40    | 60     | 100   |

#### **Outline of the Course:**

- 1 Introduction of the concept of the seminar i.e. why, when (a boost to encourage the students for its importance)
- Defining the area of interest from the enlisted pool: Applied electromagnetic engineering for industrial applications, electromagnetic waves, Linear and integrated circuits and their applications, Audio-video engineering, Power electronics-circuits and devices, Microcontroller based advanced applications and concepts, Various electronics circuits design theories-their approaches and practical implementations, Microprocessor, peripherals, communication techniques, Engineering management, Electronics Devices, components, high end technology, Network Theory, Circuits, Advanced mathematical concepts, E-software, instruments and measurements, embedded systems, speech processing, fuzzy logic and neural networks.
- 3 Searching for the reading material.
- 4 Reading the material for finalizing the topic.
- 5 Writing and re-writing the abstract/summary and report.
- 6 Preparing and finalizing the presentation in ppt format. Presenting and attending the seminar (during the semester) per batch in time multiplexed Mode.

**Total hours (Laboratory): 32** 

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## **Detailed Syllabus:**

| Sr. | Topics  |  |  |  |  |  |
|-----|---|--|--|--|--|--|
| No. |   |  |  |  |  |  |
| 1.  | Introduction of the concept of the seminar i.e. why, when (a boost to encourage the students for its importance)  |  |  |  |  |  |
| 2.  | Defining the area of interest from the enlisted pool: Applied electromagnetic engineering for industrial applications, electromagnetic waves, Linear and integrated circuits and their applications, Audio-video engineering, Power electronics-circuits and devices, Microcontroller based advanced applications and concepts, Various electronics circuits design theories-their approaches and practical implementations, Microprocessor, peripherals, communication techniques, Engineering management, Electronics Devices, components, high end technology, Network Theory, Circuits, Advanced mathematical concepts, E-software, instruments and measurements, embedded systems, speech processing, fuzzy logic and neural networks. |  |  |  |  |  |
| 3.  | Searching for the reading material.   |  |  |  |  |  |
| 4.  | Reading the material for finalizing the topic.  |  |  |  |  |  |
| 5.  | Writing and re-writing the abstract/summary and report.   |  |  |  |  |  |
| 6.  | Preparing and finalizing the presentation in ppt format. Presenting and attending the seminar (during the semester) per batch in time multiplexed Mode.   |  |  |  |  |  |

#### **Instructional Method and Pedagogy** (Continuous Internal Assessment (CIA) Scheme)

- At the start of course, the course delivery pattern, prerequisite of the subject will be discussed
- Lecture may be conducted with the aid of multi-media projector, black board, OHP etc.
   & equal weightage should be given to all topics while teaching and conduction of all examinations.
- Attendance is compulsory in lectures and laboratory, which may carries five marks in overall evaluation.
- One/Two internal exams may be conducted and total/average/best of the same may be converted to equivalent of 30 marks as a part of internal theory evaluation.
- Assignment based on course content will be given to the student for each unit/topic and will be evaluated at regular interval. It may carry an importance of ten marks in the overall internal evaluation.
- Surprise tests/Quizzes/Seminar/Tutorial may be conducted and having share of five marks in the overall internal evaluation.



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#### **Learning Outcomes:**

At the end of this course, the student would be able

- 1. Able to understand the basic importance of seminar
- 2. Able to present him/her self in front of the public i.e killing the stage fear
- 3. Able to study at his/her own in the area of interest
- 4. Develop the soft skill and personality
- 5. Able to learn multiple new techniques
- 6. Able to know the current trends in the electronics engineering

#### **Text Books and Reference Books:**

Depends upon the area of seminar chosen and finalized by the respective faculty advisor